

WHAT IS CLAIMED IS:

1. A timing adjusting apparatus comprising:

an AD converter for receiving an input signal of which
5 input signal level is converted into a digital signal to generate
a digitized input signal; and

a counter circuit to which the digitized input signal,
a counter clock signal and a trigger signal are provided so
as to set a count number based on the digitized input signal
10 and start counting the counter clock signal in response to
the trigger signal, wherein an output signal is generated from
the counter circuit at the timing of the counter clock signal
reaching the count number.

15 2. The timing adjusting apparatus according to claim
1, wherein the digitized input signal is set as the count number
when the trigger signal is input.

3. The timing adjusting apparatus according to claim
20 1 or 2, wherein said AD converter is further comprising:

a binary-coded N-bit output counter for counting an input
clock signal and carrying out a count operation repeatedly;

a DA converter for converting an N-bit output signal of
the binary-coded N-bit output counter into a counter analog
25 signal, and outputting the counter analog signal;

a comparator for comparing the input signal with the counter
analog signal to output a comparison output signal; and

a latch circuit for inputting the N-bit output signal
as data, latching the N-bit output signal, and outputting the
30 latched N-bit output signal as the digitized input signal in
accordance with a change of the comparison output signal.

4. The timing adjusting apparatus according to claim
3, wherein the counter clock signal is selected from one of
35 the N-bit output signals.